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I hereby certify that this corresponden	nce is being transmitted by facsimile the Commissioner for 1450 on June 1 2004. Eacsimile No. (703) 872-9306.	
Patents, F.O. Box 1450, Alexandra, VA 22515		
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Name of person signing this certificate	Signature and tate	•

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants: Davis, et al.

Case: 8381/ETCH/SILICON/JB1

Serial No.: Not yet assigned

Filed: March 19, 2004

Examiner: Not yet assigned

Group Art Unit: Not yet assigned

Title: METHOD FOR CONTROLLING A MULTI-PASS PROCESS FOR

FABRICATING INTEGRATED DEVICES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

me 3,200°

SIR:

STATUS INQUIRY

The Applicants request information regarding the status of the above-referenced patent application. It has been approximately 2 ½ months since the patent application was filed on March 19, 2004.

Please fax back to Moser, Patterson & Sheridan, LLP at (732) 530-9808 the anticipated date of the filing receipt.

Respectfully submitted

Keith P. TABOADA

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ANTICIPATED DATE OF THE FILING RECEIPT